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S P E C I F I C A T I O N

BE IT KNOWN THAT WE, SATOSHI KOHTAKA, TAKAFUMI HASHIGUCHI and YUKIO ENDO, all residing at c/o ADVANCED DISPLAY INC., 997, Miyoshi, Nishigoshi-machi, Kikuchi-gun, KUMAMOTO 861-1198 JAPAN, subjects of Japan, have invented certain new and useful improvements in

LIQUID CRYSTAL DISPLAY DEVICE

of which the following is a specification:-

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LIQUID CRYSTAL DISPLAY DEVICE

BACKGROUND OF THE INVENTION

The present invention relates to a liquid crystal display in which a pair of insulating substrates are bonded so as to interpose a liquid crystal layer between the pair of insulating substrates, and more particularly to a shape of inner connecting electrodes of counter electrodes.

In a liquid crystal display using an array substrate on which transfer electrodes are formed by patterning a conductive thin film that has been formed by the last step of forming a conductive film in a step of manufacturing an array substrate, the transfer electrode for supplying a common electrical potential to common electrodes on a counter substrate has a structure in which the film thickness of the laminated layers in the center portion of the transfer electrode is allowed to have the same thickness as the peripheral portion thereof.

As illustrated in Fig. 4, in the structure of the conventional transfer electrode, in the case where an electrical potential is supplied from a transfer electrode 2 on an insulating substrate 1 to a common electrode (conductive thin film 11) on a counter substrate 10 through a conductive material 9 and the common electrode potential is supplied to the transfer electrode 2 through a conductive metal film 3 formed in the second step of forming a conductive film on the insulating substrate 1, the conductive metal film 3 which is electrically connected to a conductive thin film 6 formed in the last step of forming a conductive film of the insulating substrate 1 via a contact hole 7 on the periphery of the transfer electrode 2, and as shown in Fig. 4, the conductive metal

film 3 is provided beneath the conductive thin film 6 in such a manner so as to reach substantially the center portion of opening of the transfer electrode. Accordingly, not only an insulating film 5 which is formed after the step of forming the conductive metal film formed in the first 5 step of forming a conductive film on the insulating substrate 1, but also an insulating film 4 which is formed after the step of forming the conductive metal film 3, are respectively provided beneath the conductive thin film 6 of the transfer electrode; thus, the film thickness of the laminated layer in the center portion of the transfer electrode is 10 allowed to have the same film thickness as the peripheral portion thereof.

The above-mentioned conventional arrangement has exemplified the case where a common electrode potential is supplied to the transfer electrode through the conductive metal film 3 formed in the 15 second step of forming a conductive film on the insulating substrate 1. Also in the case where the electrical potential is supplied to the transfer electrode through another conductive metal film, the conductive metal film and the insulating film are provided so as to reach substantially the center portion of the opening of the transfer electrode; therefore, the film 20 thickness of the laminated layer in the center portion of the transfer electrode is allowed to have the same film thickness as the peripheral portion thereof.

However, in the above-mentioned construction where the film thickness of the laminated layer in the center portion of the transfer electrode is allowed to have the same film thickness as the peripheral portion thereof, in the case where, upon joining to the counter substrate, compressive deformation of a conductive material applied to the transfer 25

electrode is insufficient, the cell gap in the vicinity of the transfer electrode tends to become thicker. As a result, a change in the panel transmittance locally occurs; this causes irregularity in luminance, resulting in degradation in the yield, and in the case when an attempt is
5 made to confirm deformation under compression of the conductive material from the rear face side, since the gap portion of the metal film is small and since the insulating films are interpolated, it is difficult to make an appropriate confirmation.

The present invention has been made so as to solve the
10 above-mentioned problems, and its objective is to make the cell gap in the vicinity of the transfer electrode uniform so that the local change in the panel transmittance is prevented, the uniformity of luminance is improved, and the yield is also improved. Moreover, the gap portion between the metal films is made greater and the insulating film is not
15 interpolated in the gap portion; thus, it is possible to easily confirm deformation under compression of the conductive material from the rear face side of the array substrate.

SUMMARY OF THE INVENTION

20 One aspect of the liquid crystal display of the present invention includes:

a first insulating substrate;
display pixels formed in such a manner as to be arranged in array like shape on the first insulating substrate, said display pixels
25 having pixel electrodes electrically connected to each other;

a counter substrate formed on a second insulating substrate on which common electrodes are formed;

a liquid crystal layer interposed between the first insulating substrate and the second insulating substrate, the first insulating substrate and the second insulating substrate being bonded each other;

5 a transfer electrode for supplying a common electrical potential to common electrodes on the second insulating substrate through a conductive material;

wherein the transfer electrode is formed by patterning a conductive thin film that has been formed by the last conductive film forming process of the first insulating substrate;

10 wherein a second conductive metal film, which has been formed in the second conductive film forming process of the first insulating substrate, and is connected to the common electrode potential, and the conductive thin film are connected to each other on the periphery of the transfer electrode through a contact hole or through
15 a direct contact, and the conductive thin film is directly formed on the first insulating substrate at one portion of the center portion of the opening of the transfer electrode.

In the liquid crystal display mentioned above, on a periphery of the transfer electrode, the first conductive metal film is preferably placed inside the first insulating film that is formed after the film forming process of the first conductive metal film and the second insulating film that is formed after the film forming process of the second conductive metal film that has been formed in the first conductive film forming process of the first insulating substrate.

25 The other aspect of a liquid crystal display of the present invention includes:

a first insulating substrate;

display pixels formed in such a manner as to be arranged in array like shape on the first insulating substrate, said display pixels having pixel electrodes electrically connected to each other;

a counter substrate formed on a second insulating substrate
5 on which common electrodes are formed;

a liquid crystal layer interposed between the first insulating substrate and the second insulating substrate, the first insulating substrate and the second insulating substrate being bonded to each other;

10 a transfer electrode for supplying a common electrical potential to common electrodes on the second insulating substrate through a conductive material;

wherein said transfer electrode is formed by patterning a conductive thin film that has been formed by the last conductive film forming process of the first insulating substrate;
15

wherein a first conductive metal film, which has been formed in the first conductive film forming process of the first insulating substrate, and is connected to the common electrode potential, and the conductive thin film are connected to each other on the periphery of the transfer electrode through a contact hole or through a direct contact,
20 and the conductive thin film is directly formed on the first insulating substrate at one portion of the center portion of the opening of the transfer electrode.

The still other liquid crystal display of the present invention
25 includes:

a first insulating substrate;

display pixels formed in such a manner as to be arranged in

array like shape on the first insulating substrate, said display pixels having pixel electrodes electrically connected to each other;

a second insulating substrate;

5 a counter substrate formed on the a second insulating substrate on which common electrodes are formed;

a liquid crystal layer interposed between the first insulating substrate and the second insulating substrate, the first insulating substrate and the second insulating substrate being bonded to each other;

10 a transfer electrode for supplying a common electrical potential to common electrodes on the second insulating substrate through a conductive material;

wherein said transfer electrode is formed by patterning a conductive thin film that has been formed by the last conductive film forming process of the first insulating substrate;

wherein the first conductive metal film, the second conductive metal film and the conductive thin film are connected to each other on the periphery of the transfer electrode through a contact hole or through a direct contact, and the conductive thin film is directly formed 20 on the first insulating substrate at one portion of the center portion of the opening of the second transfer electrode.

The yet other liquid crystal display of the present invention includes:

25 a thin film transistor comprising a gate electrode pattern formed on a first insulating substrate, a first insulating film coating the gate electrode, a first semiconductor pattern formed on the first insulating film, an n-type semiconductor pattern provided on the

semiconductor pattern in such a manner as to be opposed to each other, and source/drain electrode patterns respectively provided on the n-type semiconductor pattern;

a second insulating film formed on the source/drain
5 electrode pattern; and

a pixel electrode pattern provided on the second insulating film; and

a transfer electrode formed by the pixel electrode pattern;

wherein the pixel electrode pattern is connected to the
10 source/drain electrode pattern through a step for forming a hole after forming the second insulating film, and a transfer electrode;

wherein the transfer electrode supplies an electrical potential to the common electrodes on the second insulating substrate through a conductive material;

15 wherein the source/drain electrode pattern and the pixel electrode pattern are connected to each other on the periphery of the transfer electrode through a contact hole or through a direct contact, and one portion of the center portion of the opening of the transfer electrode is formed by directly forming the pixel electrode pattern on the
20 first insulating substrate.

In the liquid crystal display mentioned above, the source/drain electrode pattern is placed inside the first insulating film and the second insulating film on the periphery of the transfer electrode.

The yet other liquid crystal display of the present invention
25 includes:

a first and second insulating substrates;

a liquid crystal layer interposed between the first and second

insulating substrates;

a thin film transistor comprising a gate electrode pattern formed on the first insulating substrate, a first insulating film coating the gate electrode, a first semiconductor pattern formed on the first insulating film, an n-type semiconductor pattern that is provided on the semiconductor pattern in such a manner as to be opposed to each other, and source/drain electrode patterns respectively provided on the n-type semiconductor pattern;

10 a second insulating film formed on the source/drain electrode pattern; and

a pixel electrode pattern placed on the second insulating film; wherein the pixel electrode pattern is connected to the source/drain electrode pattern through a step for forming a hole after forming the second insulating film, and a transfer electrode formed by 15 the pixel electrode pattern;

wherein the transfer electrode supplies an electrical potential to the common electrodes on the second insulating substrate through a conductive material;

20 wherein the gate electrode pattern and the pixel electrode pattern are connected to each other on the periphery of the transfer electrode through a contact hole or through a direct contact, and one portion of the center portion of the opening of the transfer electrode is formed by directly forming the pixel electrode pattern on the first insulating substrate.

25 The yet other liquid crystal display of the present invention includes:

a first and second insulating substrates;

a liquid crystal layer interpolated between the first and second insulating substrates;

a thin film transistor comprising a gate electrode pattern formed on the first insulating substrate, a first insulating film coating 5 the gate electrode, a first semiconductor pattern formed on the first insulating film, an n-type semiconductor pattern provided on the semiconductor pattern in such a manner as to be opposed to each other, and source/drain electrode patterns respectively provided on the n-type semiconductor pattern;

10 a second insulating film formed on the source/drain electrode pattern; and

a pixel electrode pattern provided on the second insulating film;

wherein the pixel electrode pattern is connected to the 15 source/drain electrode pattern through a step for forming a hole after forming the second insulating film, and a transfer electrode formed by the pixel electrode;

wherein the transfer electrode supplies an electrical potential to the common electrodes on the second insulating substrate through a 20 conductive material;

wherein the gate electrode pattern, the source/drain electrode pattern and the pixel electrode pattern are connected to each other on the periphery of the transfer electrode through a contact hole or through a direct contact, and one portion of the center portion of the 25 opening of the transfer electrode is formed by directly forming the pixel electrode pattern on the first insulating substrate.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a cross sectional view showing a transfer electrode of EMBODIMENT 1 of the present invention;

Fig. 2 is a cross sectional view showing a transfer electrode of 5 EMBODIMENT 2 of the present invention;

Fig. 3 is a cross sectional view showing a transfer electrode of EMBODIMENT 3 of the present invention; and

Fig. 4 is a cross sectional view showing a conventional transfer electrode.

10

DETAILED DESCRIPTION

EMBODIMENT 1

Referring to Fig. 1, the following description will discuss the 15 EMBODIMENT 1 of the present invention. Fig. 1 shows a cross-cross cross sectional view of a transfer electrode in the EMBODIMENT 1 of the present invention, and the following description will discuss the construction together with the functions thereof. Here, the construction in which an electrical potential is connected to the 20 common electrodes (conductive thin film) on the opposing substrate side from the transfer electrode on the array substrate through the conductive material is the same as that shown in Fig. 4; therefore, the description thereof is omitted. Fig. 1 shows a cross-sectional shape of the transfer electrode on an array substrate side of a liquid crystal 25 display constituted by a pair of insulating substrates that are joined to each other with a liquid crystal layer interpolated in between; and reference number 1 is an insulating substrate (array substrate), 2 is a

transfer electrode, 3 is a metal film of the second layer from the substrate face and that has been formed in the second conductive film forming process of the insulating substrate 1, 4 is an insulating film of the second layer formed after the metal film 3 as the second layer, 5 is 5 an insulating film of the first layer formed after the metal film as the first layer formed in the first conductive film forming process of the insulating substrate 1, 6 is a transparent conductive thin film that is formed in the last conductive film forming process of the insulating substrate 1 and that forms the transfer electrode 2, and 7 is a contact 10 hole.

When an electrical potential is supplied to the common electrodes on the opposing substrate, a conductive material is applied to the center portion of the transfer electrode 2, and this is connected to an electrode section on the opposing substrate. Fig. 1 shows a case in 15 which a common electrical potential on the insulating substrate 1 is supplied to the transfer electrode section through the metal film 3 as the second layer that has been formed in the second conductive film forming process of the insulating substrate 1. The metal film 3 supplies the common electrical potential to the conductive thin film 6 through the 20 contact hole 7 in the vicinity of the transfer electrode 2, and the conductive thin film 6 allows at least one portion in the center portion of the transfer electrode 2 to be directly formed on the insulating substrate 1. In the present specification, one portion refers to a range of approximately 10 % to 90 % of the opening section of the transfer 25 electrode 2.

Moreover, as illustrated in Fig. 1, the metal film 3 of the second layer is placed so as to be interpolated between the insulating

films 4 and 5 of the first layer and the second layer. With this arrangement, it is possible to avoid a problem that arises when the metal film 3 is placed so as to be exposed outside from the edges of the insulating films 4 and 5. In other words, it is possible to solve the 5 problem that at the time of hole-forming processes of the insulating films 4 and 5, below the edge of the metal film 3 of the second layer, the insulating film 5 of the first layer is etched outward from the center of the transfer electrode 2 in a grooved form with the result that the coverage of the conductive thin film 6 deteriorates.

10 The above-mentioned arrangement makes it possible to make the center portion of the transfer electrode 2 thin as compared with the peripheral portion; therefore, even in the case of an insufficient deformation under compression of the conductive material, the cell gap in the vicinity of the transfer electrode is not made thicker 15 so that the cell gap in the vicinity of the transfer electrode 2 is uniformly maintained. Thus, it is possible to prevent local variations in the panel transmittance, to improve the uniformity in luminance, and also to improve the yield. Moreover, the gap portion of the metal film is made comparatively wider, and the insulating film is not 20 interpolated in the gap portion; therefore, the deformation under compression of the conductive material can be confirmed easily from the rear face of the array substrate.

EMBODIMENT 2

25 Referring to Fig. 2, the following description will discuss the EMBODIMENT 2. Fig. 2 shows a cross-cross cross sectional view of a transfer electrode in the EMBODIMENT 2 of the present invention, and

the following description will discuss the construction together with the functions thereof. Here, the construction in which an electrical potential is connected to the common electrodes (conductive thin film) on the opposing substrate side from the transfer electrode on the array substrate through the conductive material is the same as that shown in Fig. 4; therefore, the description thereof is omitted. Fig. 2 shows a cross-sectional shape of the transfer electrode on an array substrate side of a liquid crystal display constituted by a pair of insulating substrates that are joined to each other with a liquid crystal layer interpolated in between; and reference number 1 is an insulating substrate (array substrate); 2 is a transfer electrode; 4 is an insulating film of the second layer that is formed after a metal film of the second layer that has been formed in the second conductive film forming process of the insulating substrate 1; 5 is an insulating film of the first layer that is formed after a metal film of the first layer that has been formed in the first conductive film forming process of the insulating substrate 1; 6 is a conductive thin film formed in the last conductive film forming process of the insulating substrate 1; 7 is a contact hole; and 8 is a metal film of the first layer formed in the first conductive film forming process of the insulating substrate 1.

When an electrical potential is supplied to the common electrodes on the opposing substrate, a conductive material is applied to the center portion of the transfer electrode 2, and this is connected to an electrode section on the opposing substrate. Fig. 2 shows a case in which a common electrical potential on the insulating substrate 1 is supplied to the transfer electrode section through the metal film 8 as the first layer that has been formed in the first conductive film forming

process of the insulating substrate 1. The metal film 8 supplies the common electrical potential to the conductive thin film 6 through the contact hole 7 in the vicinity of the transfer electrode 2, and the conductive thin film 6 allows at least one portion in the center portion 5 of the transfer electrode 2 to be directly formed on the insulating substrate 1. Moreover, Fig. 2 has exemplified the structure in which the metal film 8 is placed under the insulating films 4 and 5, and since no insulating film that is subjected to etching is located under the metal film 8, no problem arises as to the coverage of the conductive thin 10 film 6 so that the metal film 8 may be removed at the same positions as the insulating films 4 and 5. Moreover, in the case of the metal film 8 having a shape that allows it to expose toward the center portion from the insulating films 4 and 5, it is not necessary to provide a structure having individual contact holes as shown in Fig. 2, and the metal film 8 15 is allowed to directly contact the conductive thin film 6 by removing the insulating films 4 and 5 on the exposed portion of the metal film 8 upon removing the insulating films 4 and 5 at the center portion of the opening section of the transfer electrode 2, thereby making it possible to supply the common electrical potential.

20 The above-mentioned arrangement makes it possible to make the center portion of the transfer electrode 2 thin as compared with the peripheral portion; therefore, even in the case of an insufficient deformation under compression of the conductive material, the cell gap in the vicinity of the transfer electrode is not made thicker 25 so that the cell gap in the vicinity of the transfer electrode 2 is uniformly maintained. Thus, it is possible to prevent local variations in the panel transmittance, to improve the uniformity in luminance,

and also to improve the yield. Moreover, the gap portion of the metal film is made comparatively wider, and the insulating film is not interpolated in the gap portion; therefore, the deformation under compression of the conductive material can be confirmed easily from
5 the rear face of the array substrate.

EMBODIMENT 3

Referring to Fig. 3, the following description will discuss the EMBODIMENT 3 of the present invention. Fig. 3 shows a cross-cross 10 cross sectional view of a transfer electrode in the EMBODIMENT 2 of the present invention, and the following description will discuss the construction together with the functions thereof. Here, the construction in which an electrical potential is connected to the common electrodes (conductive thin film) on the opposing substrate 15 side from the transfer electrode on the array substrate through the conductive material is the same as that shown in Fig. 4; therefore, the description thereof is omitted. Fig. 3 shows a cross-sectional shape of the transfer electrode on an array substrate side of a liquid crystal display constituted by a pair of insulating substrates that are joined to 20 each other with a liquid crystal layer interpolated in between; and reference number 1 is an insulating substrate (array substrate); 2 is a transfer electrode; 3 is a metal film of the second layer that is formed in the second conductive film forming process of the insulating substrate 1; 4 is an insulating film of the second layer that is formed after a metal 25 film 3 that has been formed in the second conductive film forming process of the insulating substrate 1; 5 is an insulating film of the first layer that is formed after a metal film 8 of the first layer that has been

formed in the first conductive film forming process of the insulating substrate 1; 6 is a conductive thin film formed in the last conductive film forming process of the insulating substrate 1; 7 is a contact hole; and 8 is a metal film of the first layer formed in the first conductive film forming process of the insulating substrate 1.

When an electrical potential is supplied to the common electrodes on the opposing substrate, a conductive material is applied to the center portion of the transfer electrode 2, and this is connected to an electrode section on the opposing substrate. Fig. 3 shows a case in which a common electrical potential on the insulating substrate 1 is supplied to the transfer electrode section through the metal film 8 as the first layer that has been formed in the first conductive film forming process of the insulating substrate 1 and the metal film 3 as the second layer that has been formed in the second conductive film forming process of the insulating substrate 1. The metal films 3 and 8 of the first and second layers supply the common electrical potential to the conductive thin film 6 through the contact hole 7 in the vicinity of the transfer electrode 2, and the conductive thin film 6 allows at least one portion in the center portion of the transfer electrode 2 to be directly formed on the insulating substrate 1. Moreover, Fig. 3 has exemplified the structure in which the metal film 8 of the first layer is placed under the insulating films 4 and 5; however, in the same manner as the EMBODIMENT 2, the metal film 8 may be removed at the same positions as the insulating films 4 and 5 on the center side of the transfer electrode. Moreover, in the case of the metal film 8 having a shape that allows it to expose toward the center portion from the insulating films 4 and 5, it is not necessary to provide a structure

having independent contact holes on the metal film 8 of the first layer as shown in Fig. 3, and the metal film 8 is allowed to directly contact the conductive thin film 6 by removing the insulating films 4 and 5 on the exposed portion of the metal film 8 upon removing the insulating films 4 and 5 at the center portion of the transfer electrode 2, thereby making it possible to supply the common electrical potential.

The above-mentioned arrangement makes it possible to make the center portion of the transfer electrode 2 thin as compared with the peripheral portion; therefore, even in the case of an insufficient deformation under compression of the conductive material, the cell gap in the vicinity of the transfer electrode is not made thicker so that the cell gap in the vicinity of the transfer electrode 2 is uniformly maintained. Thus, it is possible to prevent local variations in the panel transmittance, to improve the uniformity in luminance, and also to improve the yield. Moreover, the gap portion of the metal film is made comparatively wider, and the insulating film is not interpolated in the gap portion; therefore, the deformation under compression of the conductive material can be confirmed easily from the rear face of the array substrate.

20

EMBODIMENT 4

Referring to FIGS. 1 to 3, the following description will discuss EMBODIMENT 4 of the present invention. The present EMBODIMENT will give detailed explanations of the laminated film constructions of transfer electrode sections of EMBODIMENT 1 to EMBODIMENT 3, and the structures of the transfer electrode center portions are the same as those shown in FIGS. 1 to 3; therefore, the

description thereof is omitted. The following description will discuss the outline of the laminated film manufacturing processes of FIGS. 1 to 3. A low resistance metal, such as Al (aluminum) or Cr (chromium), was formed on an insulating substrate 1 (array substrate) as a film by means of sputtering, etc., and this was patterned by using resist formed by a normal photolithographic printing method, etc., to form a metal film 8 of the first layer forming a gate electrode. Next, an insulating film 5 of the first layer made of a silicon nitride film, etc., an i-type amorphous silicon film forming a semiconductor layer and an n-type amorphous silicon film to which impurities had been injected were successively formed by using the plasma CVD method, etc. Then, the above-mentioned i-type and n-type amorphous silicon was patterned by using resist formed by a photolithographic printing method, etc.. And after that a low resistance metal, such as Al or Cr, was formed by means of sputtering, etc. and this was patterned by using resist formed by a normal photolithographic printing method, etc., to form a metal film 3 of the second layer forming a source/drain electrode. Next, the insulating film 4 of the second layer, made of a silicon nitride film, etc. was deposited by using the plasma CVD method, etc., and contact holes 7 were formed in the above-mentioned insulating films 5 and 4 of the first and second layers by means of dry etching, etc. Simultaneously, an area in the center of the insulating substrate 1, at which the transfer electrode 2 is formed, was allowed to expose. Next, a conductive thin film 6 made of ITO (Indium Tin Oxide), etc. was formed by means of sputtering, etc., and this was patterned by using resist formed by a photolithographic printing method, etc. to form pixel electrodes and transfer electrodes. In the case when an

electrical potential is supplied to the common electrodes on the opposing substrate, a conductive material is applied to the center portion of the transfer electrode 2, and this is connected to the common electrode section on the opposing substrate.

5 In the case when, as illustrated in Fig. 1, a common electrical potential on the insulating substrate 1 is supplied to the transfer electrode section through the source/drain electrode pattern made of the metal film 3 of the second layer, the source/drain electrodes (second layer metal film 3) and the transfer electrode 2
10 (conductive thin film 6) are connected through the contact hole 7 on the peripheral portion of the transfer electrode, as shown in the Figure.

As illustrated in Fig. 2, in the case when a common electrical potential on the insulating substrate 1 is supplied to the transfer electrode section through the gate electrode pattern made of the metal film 8 of the first layer, the gate electrode (first layer metal film 8) and the transfer electrode (conductive thin film 6) are connected through the contact hole 7 on the peripheral portion of the transfer electrode, as shown in the Figure.

As illustrated in Fig. 3, in the case when a common electrical potential on the insulating substrate 1 is supplied to the transfer electrode section through the source/drain electrode pattern and the gate electrode pattern, the source/drain electrode pattern, the gate electrode pattern and the transfer electrode (conductive thin film 6) are connected through the contact hole 7 on the peripheral portion of the transfer electrode, as shown in the Figure.

The above-mentioned arrangement makes it possible to make the center portion of the transfer electrode 2 thin as compared

with the peripheral portion; therefore, even in the case of an insufficient deformation under compression of the conductive material, the cell gap in the vicinity of the transfer electrode is not made thicker so that the cell gap in the vicinity of the transfer electrode 2 is uniformly maintained. Thus, it is possible to prevent local variations in the panel transmittance, to improve the uniformity in luminance, and also to improve the yield. Moreover, the gap portion of the metal film is made comparatively wider, and the insulating film is not interpolated in the gap portion; therefore, the deformation under compression of the conductive material can be confirmed easily from the rear face of the array substrate.

Referring to EMBODIMENT 1 to EMBODIMENT 4, the present invention has been explained; however, the present invention is not intended to be limited by EMBODIMENT 1 to EMBODIMENT 4, and various modifications may of course be made within the scope of the present invention.

For example, not limited to the layer construction of the metal films and the insulating films formed on the array substrate of EMBODIMENT 1 to EMBODIMENT 4, the feature of the present invention in which the conductive thin film 6 to be connected to the conductive material 9 forming the transfer electrode is directly formed on the array substrate 1 may be applied to another substrate construction having a different film-forming process.

In the liquid crystal display of the present invention, since at least one portion of the central portion of the transfer electrode is directly formed on the insulating substrate on the array substrate side, it is possible to narrow the center portion of the transfer electrode as

compared with the peripheral portion; therefore, even in the case of an insufficient deformation under compression of the conductive material, the cell gap in the vicinity of the transfer electrode is not made thicker so that the cell gap in the vicinity of the transfer electrode is uniformly maintained. Thus, it becomes possible to prevent local variations in the panel transmittance, to improve the uniformity in luminance, and also to improve the yield. Moreover, the gap portion of the metal film is made comparatively wider, and the insulating film is not interpolated in the gap portion; therefore, the deformation under compression of the conductive material can be confirmed easily from the rear face of the array substrate.